

<b>Notice of References Cited</b>	Application/Control No. 10/710,398	Applicant(s)/Patent Under Reexamination LEI ET AL.	
	Examiner MICHAEL YAARY	Art Unit 2193	Page 1 of 1

#### U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

#### FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

#### NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Lai et al, A Hybrid Number System Processor with Geometric and COmplex Arithmetic Capabilities, August 1991, Volume 40, No. 8, Pages 952-962
	V	Lei et al, DSP Architecture with Jumping-Floating-Points Number System, Asian Solid-State Circuits Conference, 2005, Pages 405-408
	W	Reza et al, A Hybrid Number System and its Application in FPGA-DSP Technology, 2004 Proceedings ITCC 2004, Volume 2, Pages 342-346
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.